

RESUME

PAGIDELA YAMUNA

Assistant Professor Adhoc

Department Electrical and Electronics Engineering

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Maid I.D: yamuna.pagidela@gmail.com

Date of Birth: 05/05/1992



OBJECTIVE:

To get associated with a progressive organization that gives me scope to update my knowledge and skills in accordance with the latest technologies and employ my professional and communication skills to contribute resourcefully towards the growth of the organization.

TEACHING EXPERIENCE: 7 years

- Working as Assistant Professor Adhoc in JNTUA college of Engineering Pulivendula from 25/06/2016 to till date.

EDUCATION QUALIFICATION:

Course	Year	Name of the institution	Percentage awarded
Ph., D.	Pursuing	JNTUA COLLEGE OF ENGINEERING, ANANTAPUR	
M. Tech (EPS)	2015	ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCE	90.8%
B.Tech	2013	SREE VIDYANIKETHAN ENGINEERING COLLEGE	82.64%
Diploma	2010	LOYOLA POLYTECHNIC	90.5%
SSC	2007	NAGARJUNA HIGH SCHOOL	89.8%

PERSONAL SKILLS:

- Established interpersonal and communication skills.
- Shows good interest in teaching and reaching higher goals in career.
- Hard working and intend to learn the new things.

AREAS OF INTEREST:

- Power systems
- Control systems
- Electrical machines

SUBJECTS TAUGHT:

Electrical machines-I, Microprocessors and Micro controllers, Electrical Circuits-I, Modern Control Theory, Electrical Measurements, Utilization of Electrical Energy, Electrical Engineering Materials, Digital Control Systems

LABS HANDLED:

Electrical Machines-II, Basic Electrical Engineering, Electrical Measurements, Power Electronics and Simulation, Electrical Technology Lab, Renewable Energy Lab

Scopus/Orcid/web of science/Vidwan Details :

Scopus ID: 35738271600

Vidwan ID: 321242

ORCID ID: 0000-0003-4912-0954

WORKSHOPS ATTENDED:

- One day National level work shop on SCADA organized by PAHSOR - 2023 at J.N.T.U.A. College of Engineering Pulivendula – 16th March 2023.
- One day National level work shop on DESIGN OF ELECTRICAL VEHICLES BY USING MATLAB organized by PAHSOR - 2023 at J.N.T.U.A. College of Engineering Pulivendula – 16th March 2023.
- One week National level work shop on ECAD for Buildings organized by Dept. of EEE, J.N.T.U.A. College of Engineering Pulivendula – 10th to 16th October 2022.
- One Day Workshop on “AWARENESS ON IEEE/IET ELECTRONIC DIGITAL LIBRARY SOURCES” Organized by the Department of EEE, JNTUACE, Anantapur on 30th November 2018.
- Three Days Training Program on “PAPER AND ARTICLE WRITING WITH LATEX” Organized by the Department of Electrical and Electronics Engineering, JNTUACEP, Pulivendula from 17th to 19th March 2017
- Two Days Professional Development Program on “GRAPHICAL SYSTEM DESIGN APPROACH IN ENGINEERING EDUCATION & RESEARCH” Organized by the

Department of Electrical and Electronics Engineering, JNTUACEP, Pulivendula on 19th & 20th August 2016.

- Two Days National Level Workshop on “SIMULATION OF POWER SYSTEMS USING NEPLAN SOFTWARE” Organized by the Department of EEE, AITS, Rajampet on 10th & 11th October 2014.

CONFERENCES:

- Presented on “INTEGRATION OF AC AND DC MICROGRIDS USING AN ANALYTICAL APPROACH” by IEEE MysuruCon 2022 16th & 17th October 2022.
- Attended one day online conference on “DESIGN OF BI-DIRECTIONAL CHARGER FOR ELECTRIC VEHICLE” organized by ICACCM 10th November 2022.
- Presented on “IMPLEMENTATION OF ANFIS CONTROLLER FOR OFF-BOARD ELECTRIC VEHICLE CHARGING” organized by ICRTET-2022 on 29th December 2022.
- Presented on “DESIGN AND IMPLEMENTATION ANFIS CONTROLLER FOR GRID CONNECTED SOLAR PV-BATTERY AND DIESEL GENERATOR BASED EV CHARGING STATION” organized by ICRTET-2022 on 29th December 2022.

FDP's ATTENDED:

- Five days online FDP on “Recent Advances in Electrical Engineering” organized by G.Pulla Reddy Engineering College from 17th to 21st October 2022.
- Three days FDP on “Engineering Graphics Through AutoCAD Software” jointly organized by ROBOGYAN 2K23 and Dept of Mechanical Engineering JNTUA College of Engineering Pulivendula from April 21st to 23rd, 2023.

Conference Publications, National & International Journals:

- Pagidela Yamuna, N. Visali “INTEGRATION OF AC AND DC MICROGRIDS USING AN ANALYTICAL APPROACH” IEEE Mysurucon - 2022.
- Yasmin, S., Kumar, G.N., Rafi, V., Yamuna, P. and Sailaja, K., 2022, November. “Design of Bi-Directional Charger for Electric Vehicle”. In 2022 International Conference on Advances in Computing, Communication and Materials (ICACCM) (pp. 1-6). IEEE.
- Sailaja.K, Yamuna. P, Rafi.V UGC listed Mukta Shabd journal title “Portable water condenser using Peltier Module” in September 2022.
- Yamuna, P., Gayatri, K., Meena, S., Kumar, S.H., Hemanth, T., Rafi, V., Pooja, A. and Sailaja, K. Juni Khyat , “MODREN BLIND STICK BY USING SUITABLE SENSORS” in September 2022.
- S. Vijay Kumar, Pagidela Yamuna “Controlling Of Transformer-less UPFC With Cascaded Multilevel Inverter” International Journal of Engineering Research in Electrical and Electronic Engineering (IJEREEE) in September 2017.
- Hemakesavulu.O, Bhaskara Prasad. P, Yamuna. P, “Fuzzy based multi string seven level inverter for photovoltaic system” International Journal of Applied Engineering Research 10(17), pp. 38491-38497.

DEPARTMENT ACTIVITIES:

- Project Coordinator
- NAAC supporting staff
- Class Teacher

PERSONAL DETAILS:

Name	: Pagidela Yamuna
Date Of Birth	: 05-05-1992
Father Name	: P. Malleswara Reddy
Sex/Marital Status	: Female/Married
Nationality	: Indian
Languages Known	: Telugu, English
Aadhaar No	816234473301
Contact Address	: D No 4-7-272, SBI colony 3rd line, Pulivendula.
Permanent Address	: D No 4-7-272, SBI colony 3rd line, Pulivendula.

DECLARATION:

I hereby declare that the above written particulars are true to the best of my Knowledge.

P. YAMUNA